

ISSUE CLASSIFICATION	
Class	Subclass

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PATENT NUMBER

U.S. UTILITY Patent Application

<p>O.I.P.E.</p> <p>TR.</p> <p>SCANNED <i>MSD</i> Q.A. <i>AA</i></p>	<p>PATENT DATE</p>
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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/773874		700 <i>710</i>	<i>240</i>	<i>2121</i>	<i>Mason</i>

APPLICANTS

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System on a chip having system bus, external bus, and bus arbiter with programmable priorities for both buses, software, and method for assigning programmable priorities

PTO-2040
12/89

ISSUING CLASSIFICATION

[illegible]

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	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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